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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Aptina Imaging CorporationApplication No./Patent No.: 10/662,445 Filed/Issue Date: September 16, 2003Entitled: METHOD AND APPARATUS FOR CONTROLLING INTEGRATION TIME IN IMAGERS

Aptina Imaging Corporation, a Corporation
 (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.
 (The extent (by percentage) of its ownership interest is _____ %)

in the patent application/patent identified above by virtue of either:

- A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

OR

- B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as follows:

1. From: Agan, Martin J. To: Micron Technology, Inc.
 The document was recorded in the United States Patent and Trademark Office at Reel 014509, Frame 0161, or for which a copy thereof is attached.
2. From: Micron Technology, Inc. To: Aptina Imaging Corporation
 The document was recorded in the United States Patent and Trademark Office at Reel 022087, Frame 0893, or for which a copy thereof is attached.
3. From: _____ To: _____
 The document was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

- ☒ As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.


 Signature

January 15, 2009
 Date

Thomas J. D'Amico
 Printed or Typed Name

(202) 420-2232
 Telephone Number

Attorney for Assignee
 Title

TO:DICKSTEIN SHAPIRO LLP COMPANY:1825 EYE STREET, NW

**UNITED STATES PATENT AND TRADEMARK OFFICE**UNDER SECRETARY OF COMMERCE FOR INTELLECTUAL PROPERTY AND
DIRECTOR OF THE UNITED STATES PATENT AND TRADEMARK OFFICE

500751092

JANUARY 12, 2009

PTAS

DICKSTEIN SHAPIRO LLP
1825 EYE STREET, NW
WASHINGTON, DC 20006UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 571-272-3350. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, MAIL STOP: ASSIGNMENT SERVICES BRANCH, P.O. BOX 1450, ALEXANDRIA, VA 22313.

RECORDATION DATE: 01/12/2009

REEL/FRAME: 022087/0893

NUMBER OF PAGES: 9

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).
DOCKET NUMBER: M4065.0882/P882ASSIGNOR:
MICRON TECHNOLOGY, INC.

DOC DATE: 09/26/2008

ASSIGNEE:
APTINA IMAGING CORPORATION
C/O CITCO TRUSTEES (CAYMAN)
LIMITED, REGATTA OFFICE PARK,
WEST BAY ROAD
GRAND CAYMAN, CAYMAN ISLANDS
Y1-
1205

SERIAL NUMBER: 10662445

FILING DATE: 09/16/2003

PATENT NUMBER:

ISSUE DATE:

TITLE: METHOD AND APPARATUS FOR CONTROLLING INTEGRATION TIME IN IMAGERS

USPTO

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ASSIGNMENT SERVICES BRANCH
PUBLIC RECORDS DIVISION

TO:DICKSTEIN SHAPIRO LLP COMPANY:1825 EYE STREET, NW

PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.101/12/2009
500751092

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Micron Technology, Inc.	09/26/2008
RECEIVING PARTY DATA	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	10662445
CORRESPONDENCE DATA	
Fax Number:	(202)420-2201
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	202-420-2200
Email:	parsonse@dicksteinshapiro.com
Correspondent Name:	Dickstein Shapiro LLP
Address Line 1:	1825 Eye Street, NW
Address Line 4:	Washington, DISTRICT OF COLUMBIA 20006
ATTORNEY DOCKET NUMBER:	M4065.0882/P882
NAME OF SUBMITTER:	Thomas J. D'Amico
Total Attachments: 7 source=M40650882Assign#page1.tif source=M40650882Assign#page2.tif source=M40650882Assign#page3.tif source=M40650882Assign#page4.tif source=M40650882Assign#page5.tif	

OP \$40.00 10662445

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PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED], Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. **General**

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: [Signature]

Date: 9/20/08, 2008

Notarization

State of Idaho

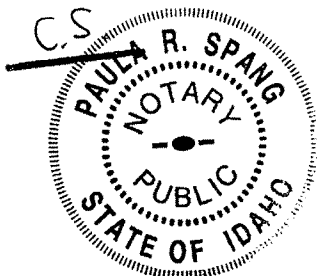
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven B. Appleton

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]
My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: Thomas L. Lauer, Jr.

Date: 9/26/08, 2008

Notarization

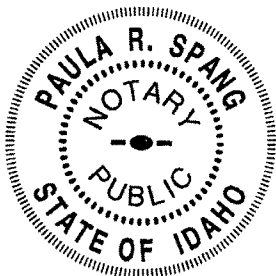
State of Idaho

County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Lauer, Jr.

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]
My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "B"
IMAGING PATENT APPLICATIONS

Aptina Applications

FILE NUMBER	TITLE	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
	MULTIPLICATION LAYER AND LOW VOLTAGE READOUT ELECTRONICS				
2002-0595.01/CN	CMOS APS WITH STACKED AVALANCHE MULTIPLICATION LAYER AND LOW VOLTAGE READOUT ELECTRONICS	Pending	China	2008101094617	Aug 22, 2003
2002-0595.02/US	CMOS APS WITH STACKED AVALANCHE MULTIPLICATION LAYER AND LOW VOLTAGE READOUT ELECTRONICS	Pending	United States of America	12078643	Apr 2, 2008
2002-0597.00/US	CMOS IMAGE SENSOR APPARATUS WITH ON-CHIP REAL-TIME PIPELINED JPEG COMPRESSION MODULE	Pending	United States of America	10227809	Aug 27, 2002
2002-0597.01/US	CMOS IMAGE SENSOR APPARATUS WITH ON-CHIP REAL-TIME PIPELINED JPEG COMPRESSION MODULE	Abandoned	United States of America	11524534	Sep 21, 2006
2002-0698.00/JP	WIDE DYNAMIC RANGE PINNED PHOTODIODE ACTIVE PIXEL SENSOR (APS)	Pending	Japan		Nov 14, 2002
2002-0698.00/US	WIDE DYNAMIC RANGE PINNED PHOTODIODE ACTIVE PIXEL SENSOR (APS)	Pending	United States of America	10294686	Nov 15, 2002
2002-0955.00/US	METHOD AND APPARATUS FOR CONTROLLING INTEGRATION TIME IN IMAGERS	Pending	United States of America	10662445	Sep 16, 2003
2002-1008.02/US	TOTAL INTERNAL REFLECTION CMOS IMAGER	Abandoned	United States of America	11293504	Dec 5, 2005
2002-1024.00/US	METHOD AND CIRCUIT FOR DETERMINING THE RESPONSE CURVE KNEE POINT IN ACTIVE PIXEL IMAGE SENSORS WITH EXTENDED DYNAMIC RANGE	Allowed	United States of America	10633673	Aug 5, 2003
2002-1024.01/US	METHOD AND CIRCUIT FOR	Pending	United States of America	10785548	Apr 18, 2007